

IN THE SPECIFICATION:

Please amend the paragraph beginning at page 1, line 4, as follows.

5 The present invention is related to United States Patent Application Number
09/785,604 entitled "Method and Apparatus for Distributing a Self-Synchronized Clock to Nodes
on a Chip," (~~Attorney Docket Number Lee 14-5-3~~), United States Patent Application Number
09/788,582 entitled "Method and Apparatus for Transferring Multi-Source/Multi-Sink Control
Signals Using a Differential Signaling Technique," (~~Attorney Docket Number Fernando 9-11-4~~),
10 United States Patent Application Number 6,754,748 B2 entitled "Method and Apparatus for
Distributing Multi-Source/Multi-Sink Control Signals Among Nodes on a Chip," (~~Attorney
Docket Number Fernando 10-12-5~~) and United States Patent Application Number 09/785,592
entitled "On-Chip Method and Apparatus for Transmission of Multiple Bits Using Quantized
Voltage Levels," (~~Attorney Docket Number Lee 15-6~~), each filed contemporaneously herewith,
15 assigned to the assignee of the present invention and incorporated by reference herein.